

PATENT NUMBER

O.I.P.E. _____ K.W. SCANNED <u>CR3</u> O.A. <u>MC</u>	PATENT DATE _____
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TC-2
JC-859

APPLICATION NO. 09/671117	CONT/PRIOR F	CLASS 711	SUBCLASS 145	ART UNIT 2186	EXAMINER H. Kim
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APPLICANTS

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NOTE

Method of controlling a cache memory to increase in access speed to a main memory, and a computer using the method

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner) _____ (Date)			Amount Due	Date Paid
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